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PTO/SB/21 (12-97)  
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# TRANSMITTAL FORM

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BOX: NON-FEE AMENDMENT

Express Mail  
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Application / Conf. No.	10/090,239 / 6539
Filing Date	March 01, 2002
First Named Inventor	Atul V. Ghia
Examiner Name	Unknown
Group Art Unit	2819
Issue Fee Batch No.	
Attorney Docket Number	X-1061 US

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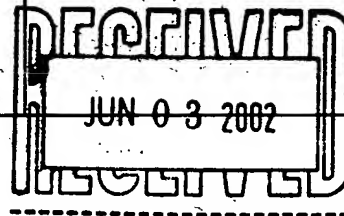
## ENCLOSURES (check all that apply)

- ☐ Fee Transmittal Form
- ☐ Amendment / Response
  - ☐ Preliminary Amendment
  - ☐ After Final
  - ☐ Affidavit(s)/declaration(s)
- ☐ Extension of Time Request
- ☐ Change Status to LARGE ENTITY
- ☐ Express Abandonment Request
- ☒ Information Disclosure Statement (Supplemental)
- ☒ Substitute PTO-1449(s) IDS by Applicant (PTO/SB/08A)
- ☐ Certified Copy of Priority Document(s)
- ☐ Response to Missing Parts/Incomplete Application
  - ☐ Response to Missing Parts under 37 CFR 1.52 or 1.53

- ☐ Assignment Papers (with Recordation Cover Sheet)
- ☐ Declaration / Oath
- ☐ Drawing(s)
- ☐ Licensing-related Papers
- ☐ Petition -
- ☐ To Convert a Provisional Application
- ☐ Power of Attorney, Revocation Change of Correspondence Address
- ☐ Terminal Disclaimer
- ☐ Request for Refund

- ☐ After Allowance Communication to
- ☐ Appeal Communication to Board of Appeals and Interferences
- ☐ Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
- ☐ Status Letter
- ☒ Return Receipt Postcard
- ☒ Additional Enclosure(s) (please identify below):

Copy of each cited reference - twelve (12)



Remarks

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Customer Number	24309 (Customer Number)		Reg. Number 35,477
Attn:	H. C. Chan		
Signature			
Date	May 20, 2002	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040	

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D. C. 20231 on this date: May 20, 2002

Typed or Printed Name	Pat Slaback		
Signature		Date	May 20, 2002

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PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Atul V. Ghia et al.  
Assignee: Xilinx, Inc.  
Title: "Low Jitter Clock for a Physical Media Access Sublayer on a Field Programmable Gate Array"  
Serial No.: 10/090,239 File Date: 03-01-02  
Examiner: Unknown Art Unit: 2819  
Docket No.: X-1061 US Conf. No.: 6539

COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the twelve (12) references listed in the attached Form PTO-1449. A copy of each is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

H. C. Chan  
Attorney for Applicants  
Reg. No. 35,477

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Pat Slaback  
Name

  
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